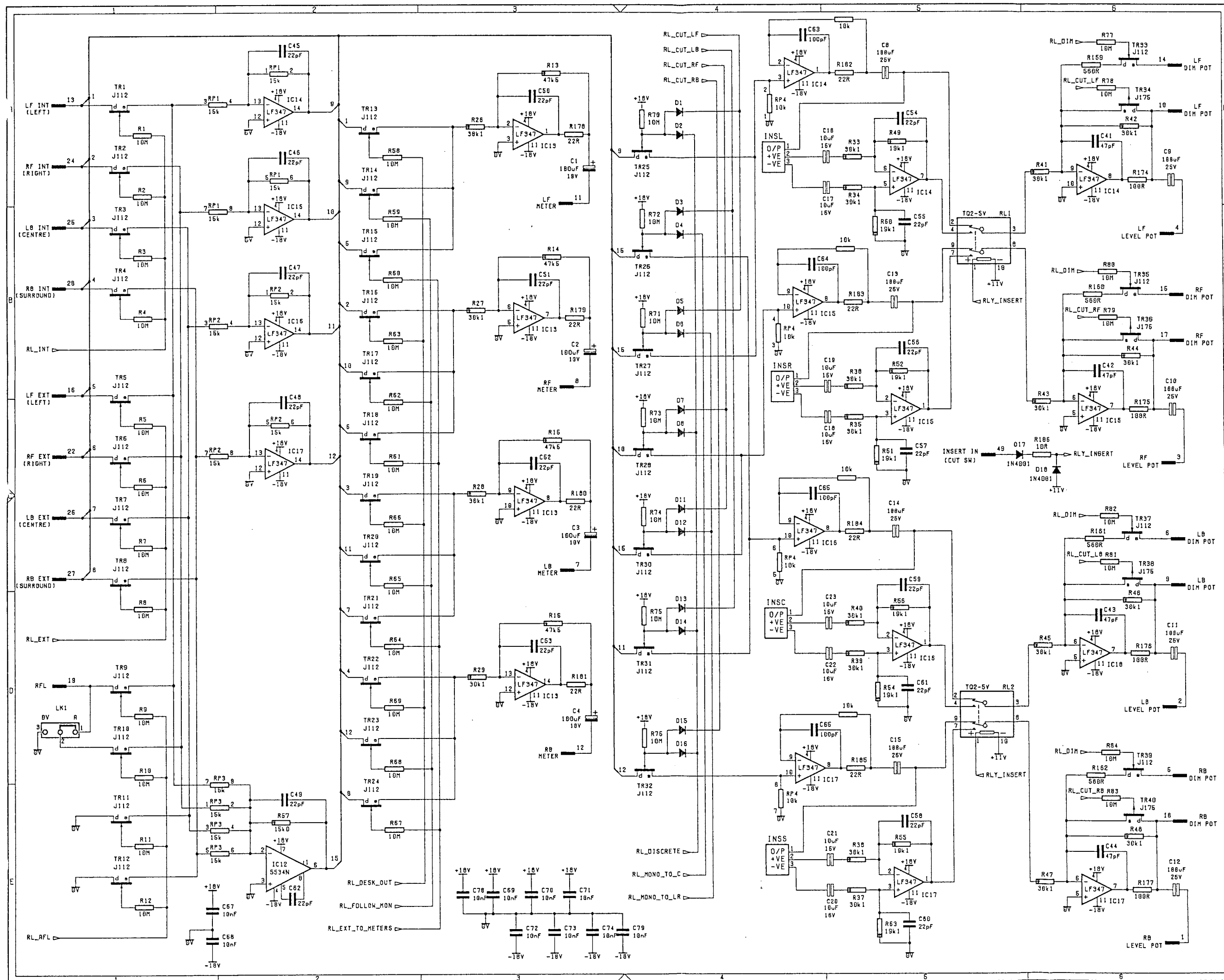




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REV	ISS	DATE	DETAILS	
0	A	21 OCT 91	NEW DRAWING	BC
1	A	28 NOV 91	ECO 4K/890 NO CHANGE	BC MM
2	A	23 SEP 92	ECO 4K/961 TRACK CUTS & 4 10K RESISTORS ADDED	MM
3	A	23 SEP 92	ECO 4K/1033 NO CHANGE	MM
4	A	23 SEP 92	ECO 4K/1039 NO CHANGE	MM

PCB ISSUE EFGH

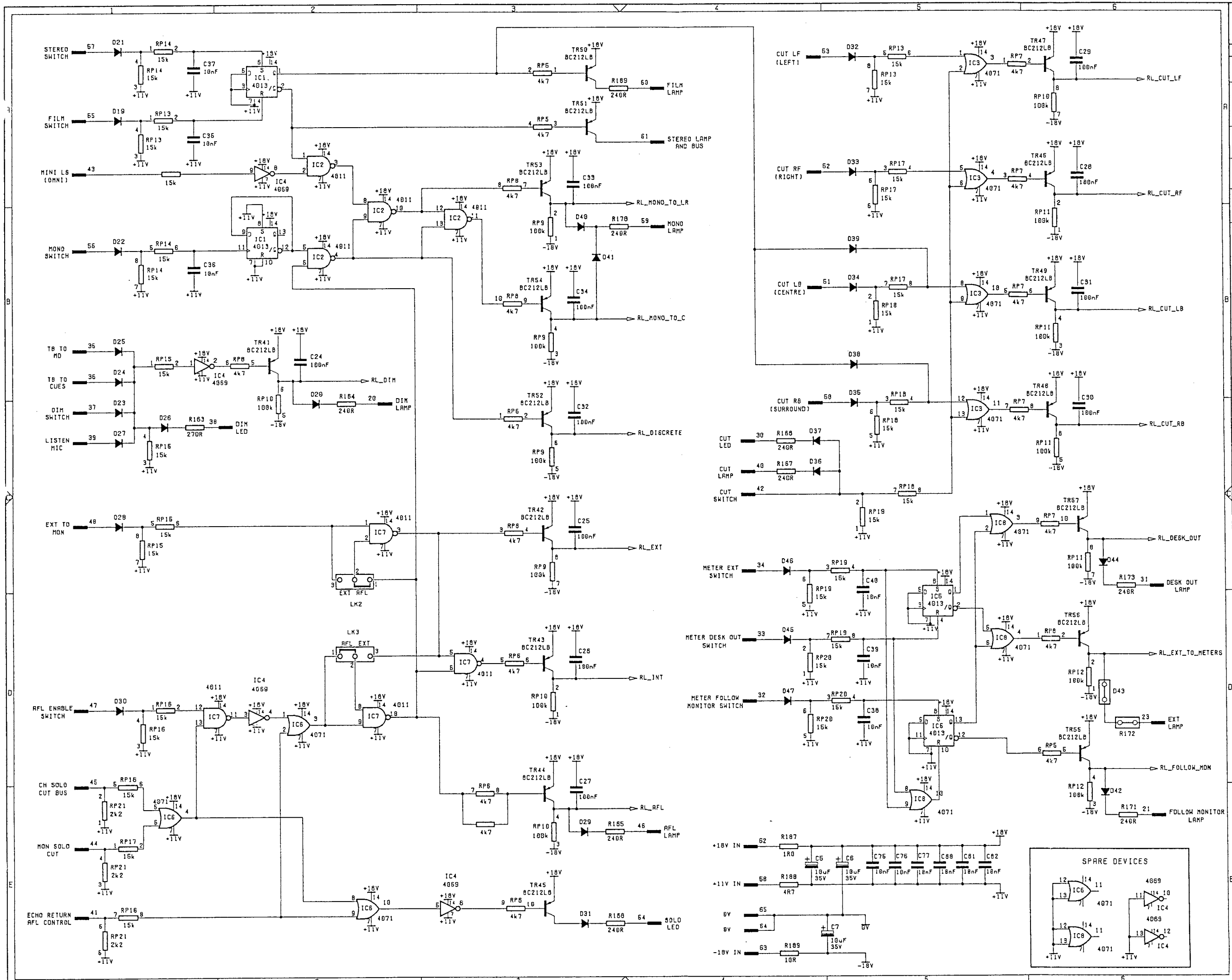
USED ON 651G A2

TITLE
FILM MONITOR MATRIX

DRG.NO. T82274.71

SHEET 1 OF 2

Solid State Logic



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REV	ISS	DATE	DETAILS	
0	A	21 OCT 91	NEW DRAWING	BC
1	A	28 NOV 91	ECO 4K/890 R172 WAS 240R D43 WAS DIODE NOW BOTH LINKS	MM
2	A	23 SEP 92	ECO 4K/961 NO CHANGE	MM
3	A	23 SEP 92	ECO 4K/1033 R187 WAS 4R7 NOW 1R (FUSIBLE)	MM
4	A	23 SEP 92	ECO 4K/1039 4K7 ADDED ACROSS RP6 PINS 7 & 8 MM	MM
PCB ISSUE EFGH				
USED ON SL651G A2				
TITLE FILM MONITOR MATRIX				
DRG.NO. T82274.72				
SHEET 2 OF 2				
Solid State Logic				